

FIG. 1

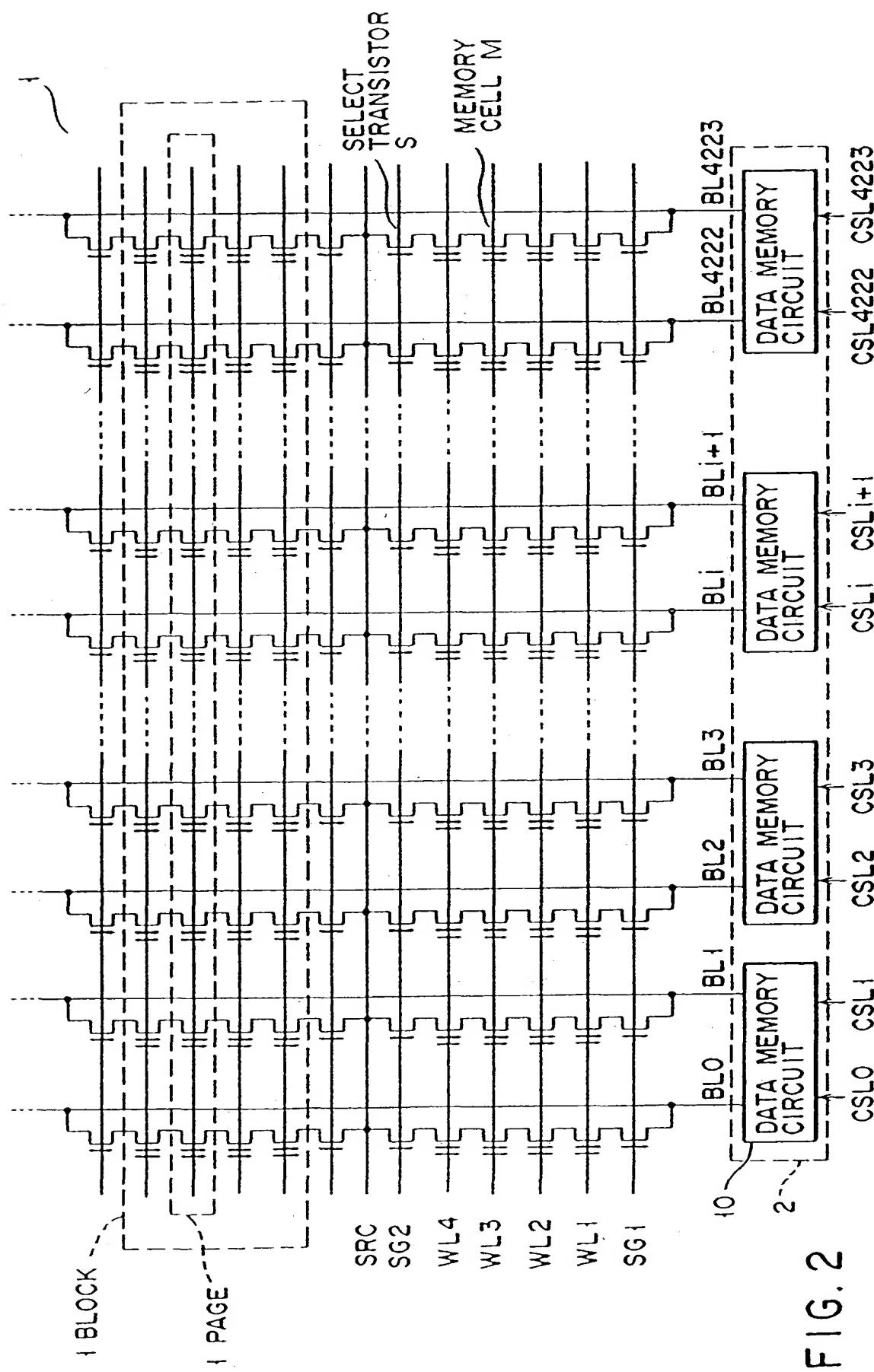


FIG. 2

MEMORY CELL M

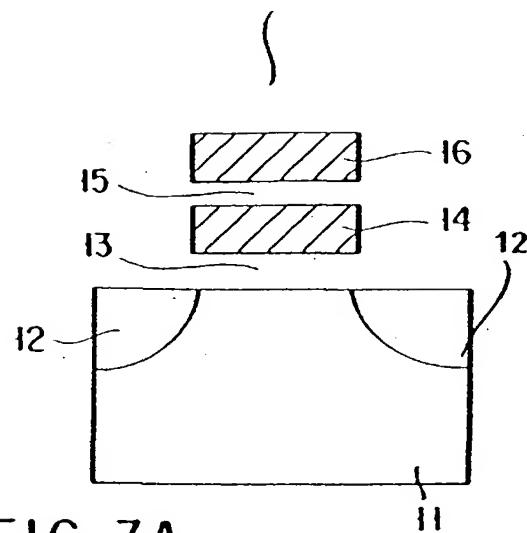


FIG. 3A

SELECT TRANSISTOR S

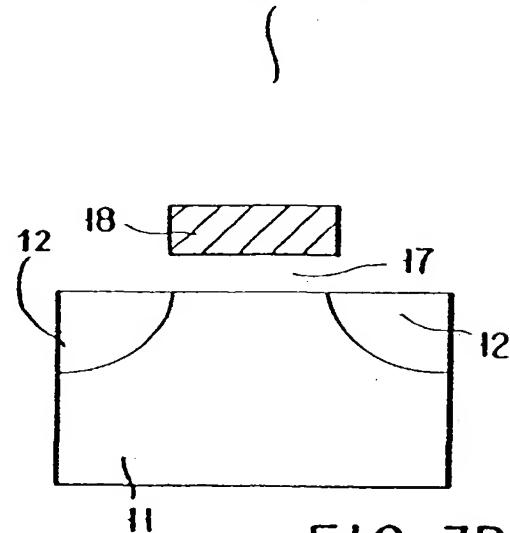


FIG. 3B

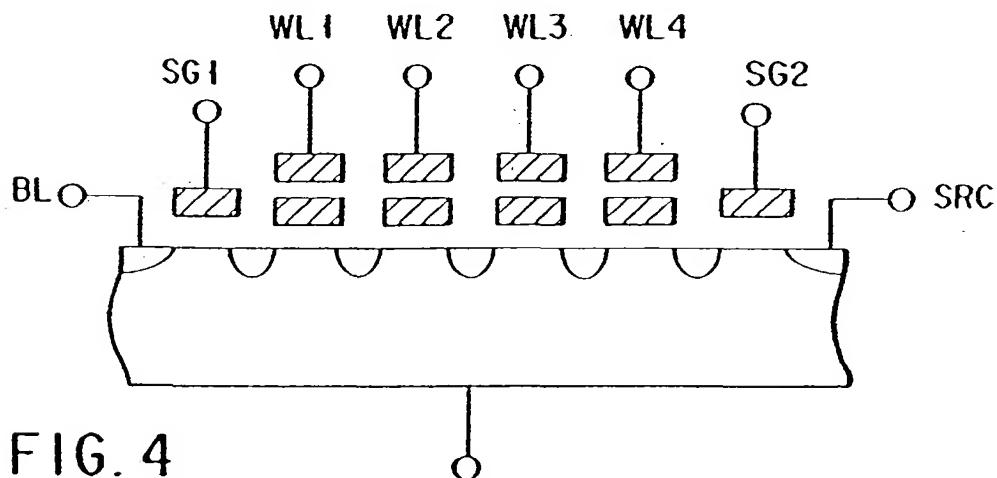


FIG. 4

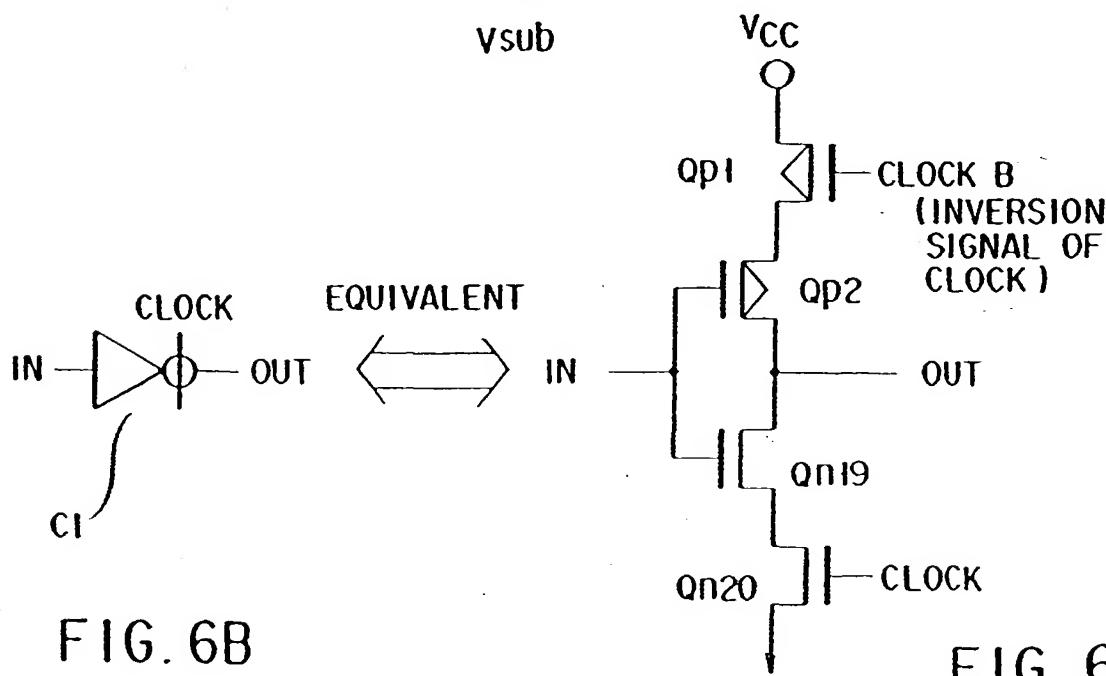
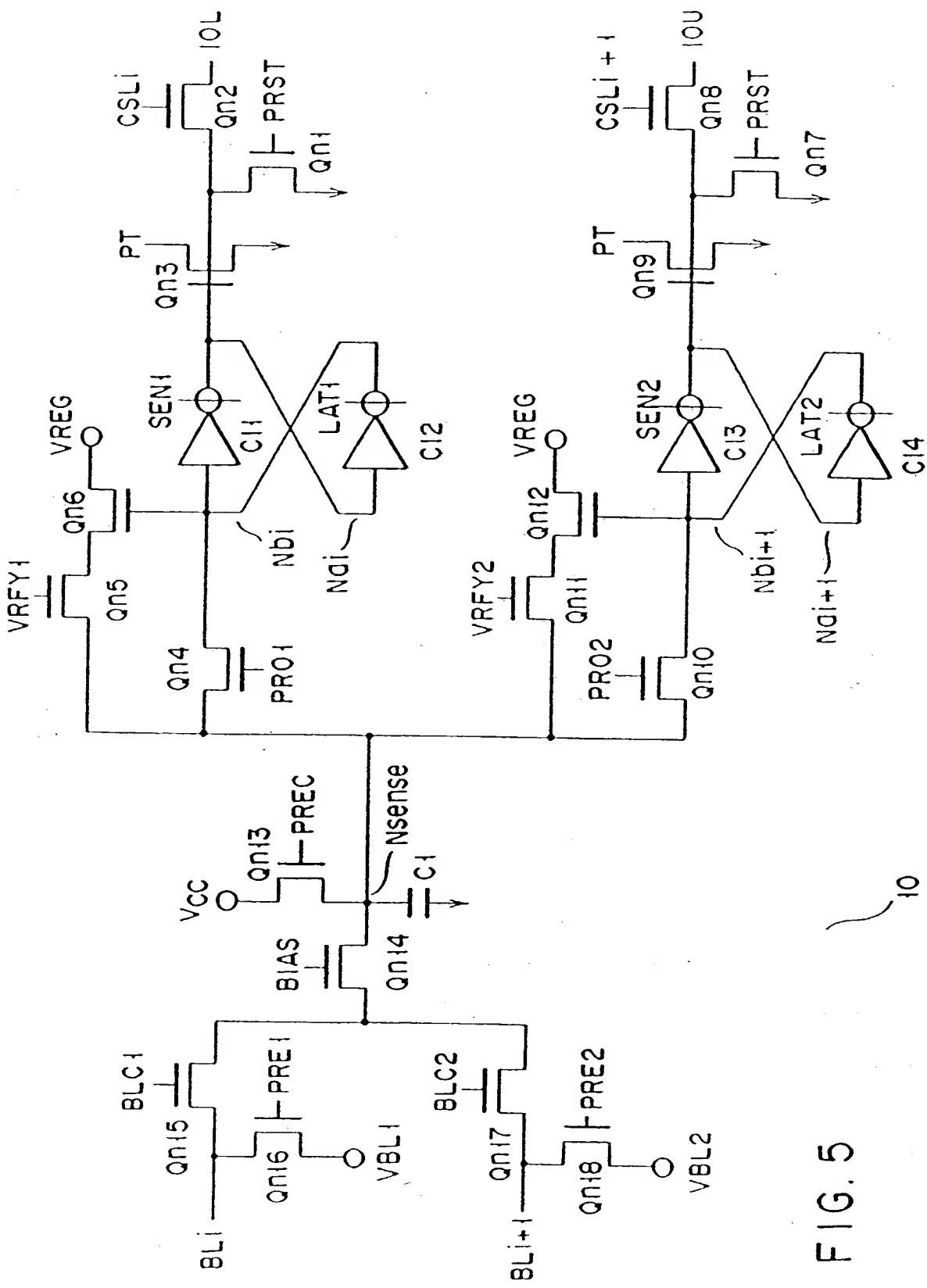


FIG. 6B



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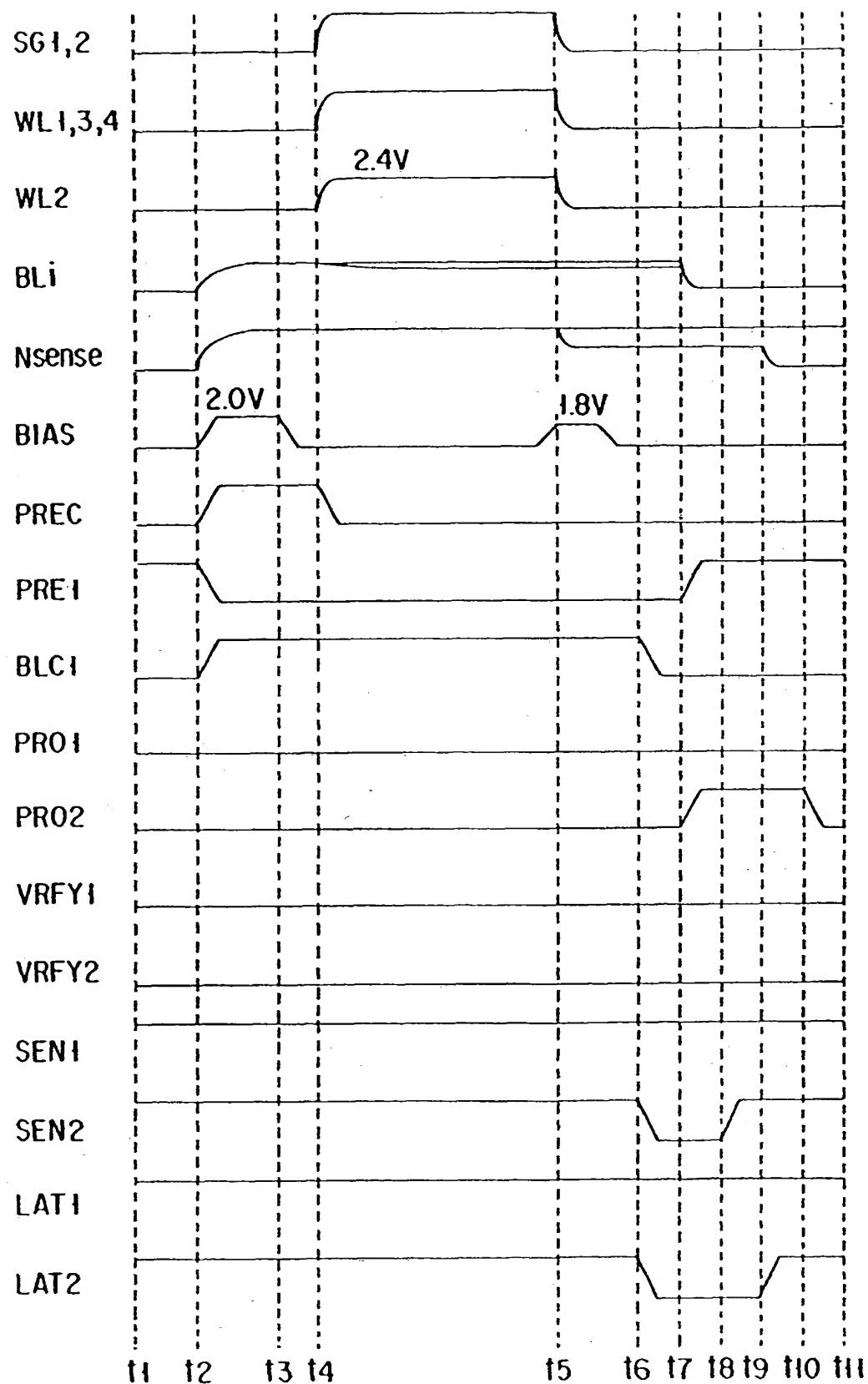


FIG. 7

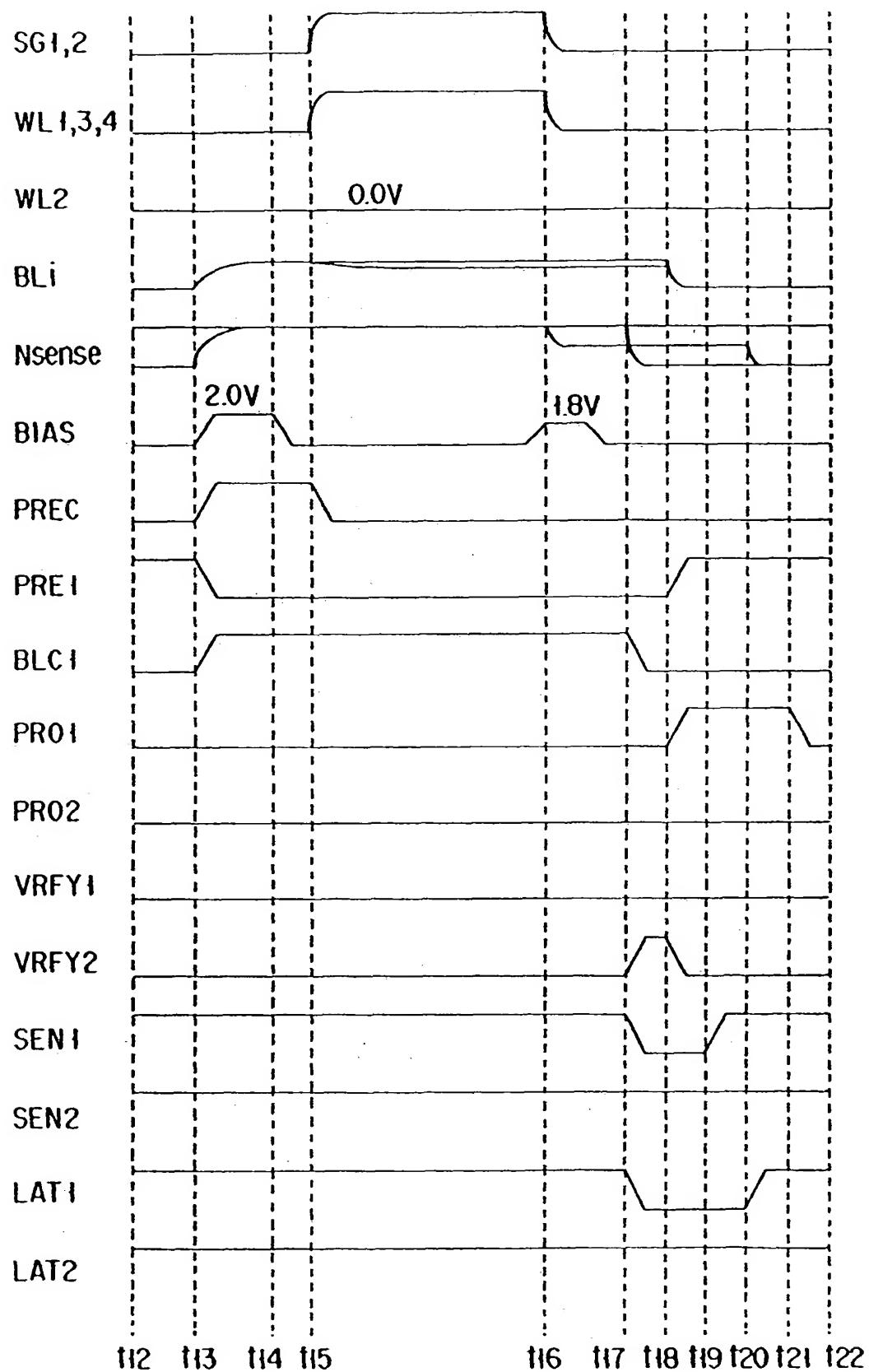


FIG. 8

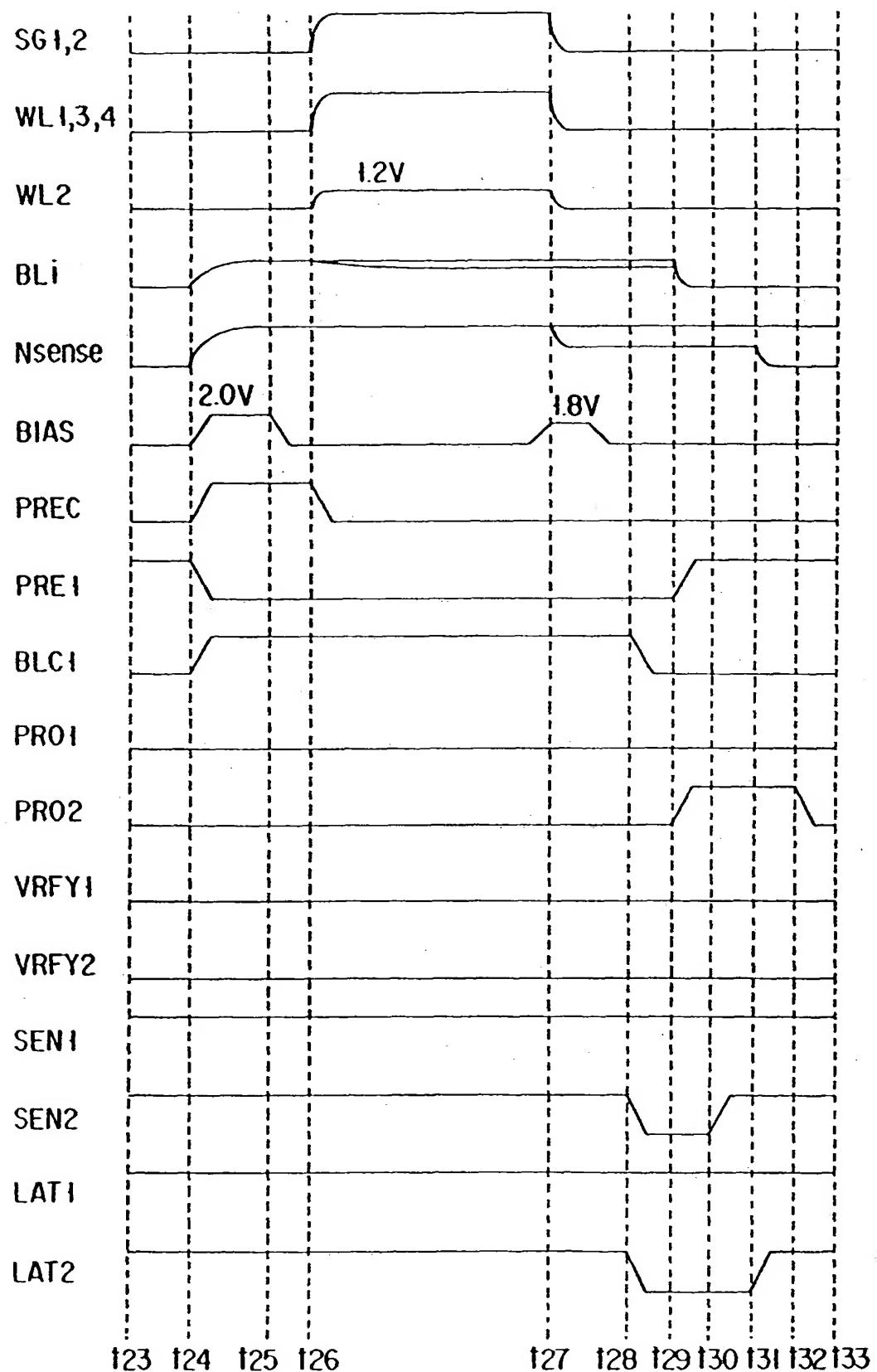


FIG. 9

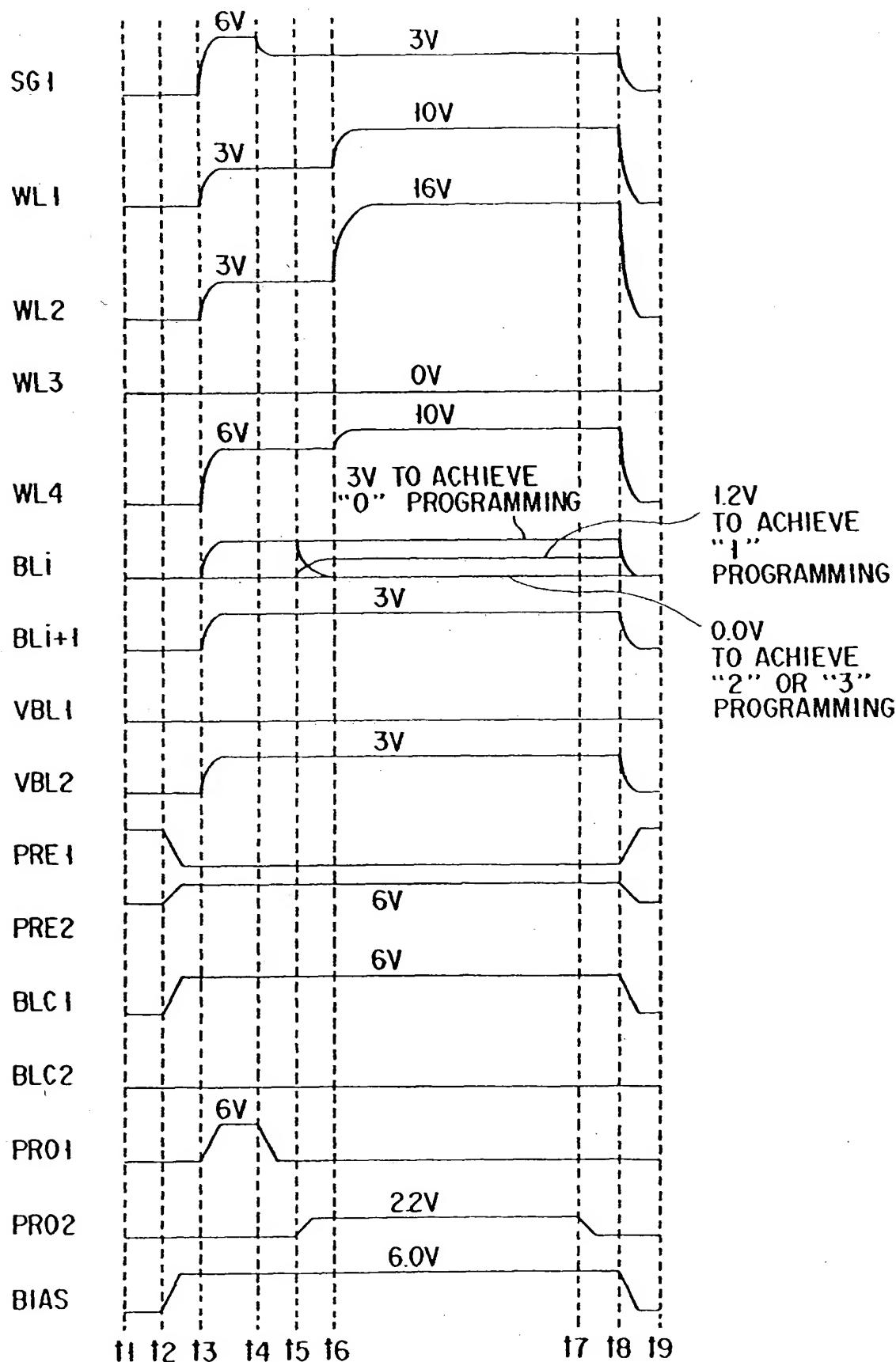


FIG. 10

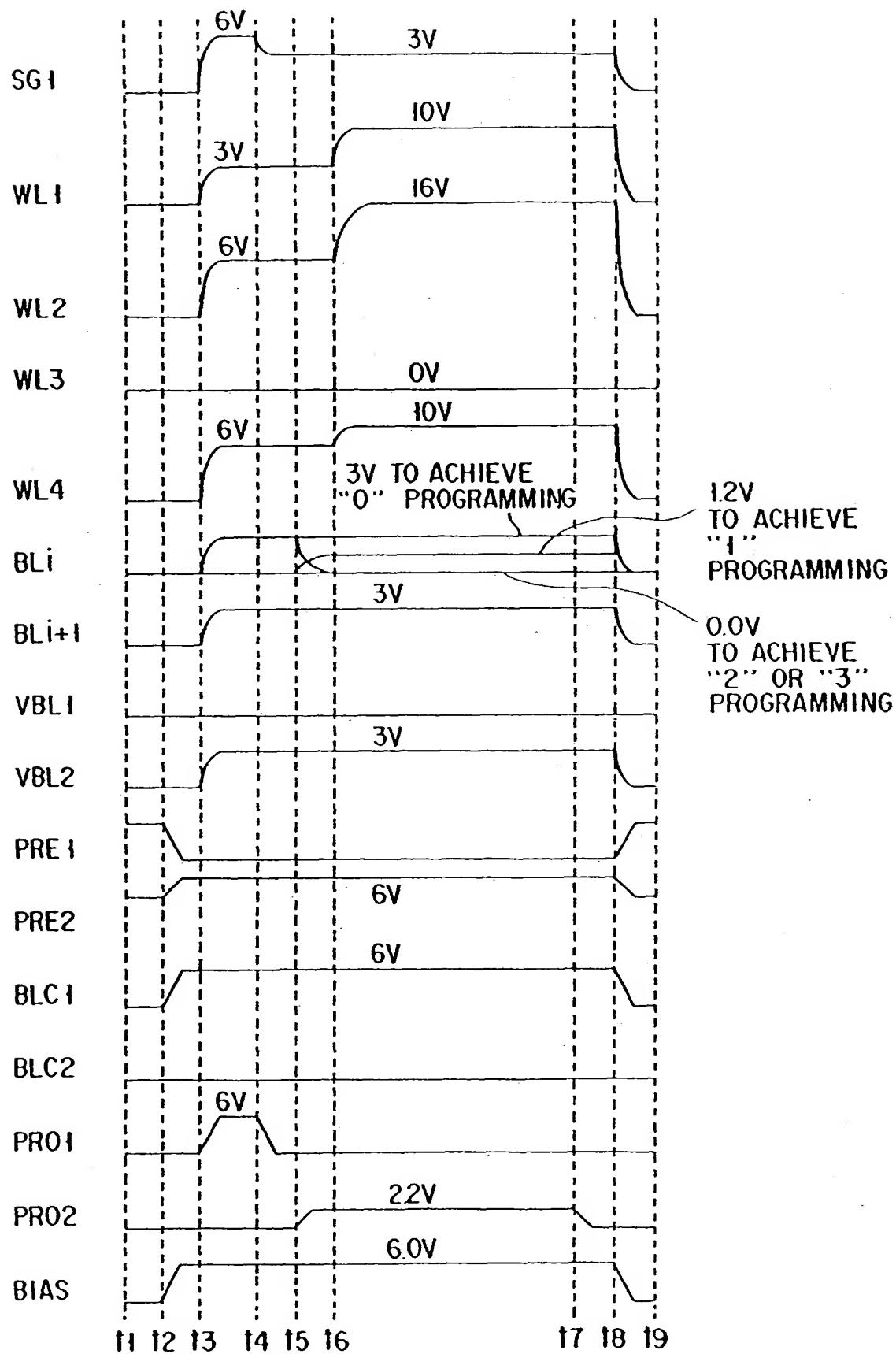


FIG. 11

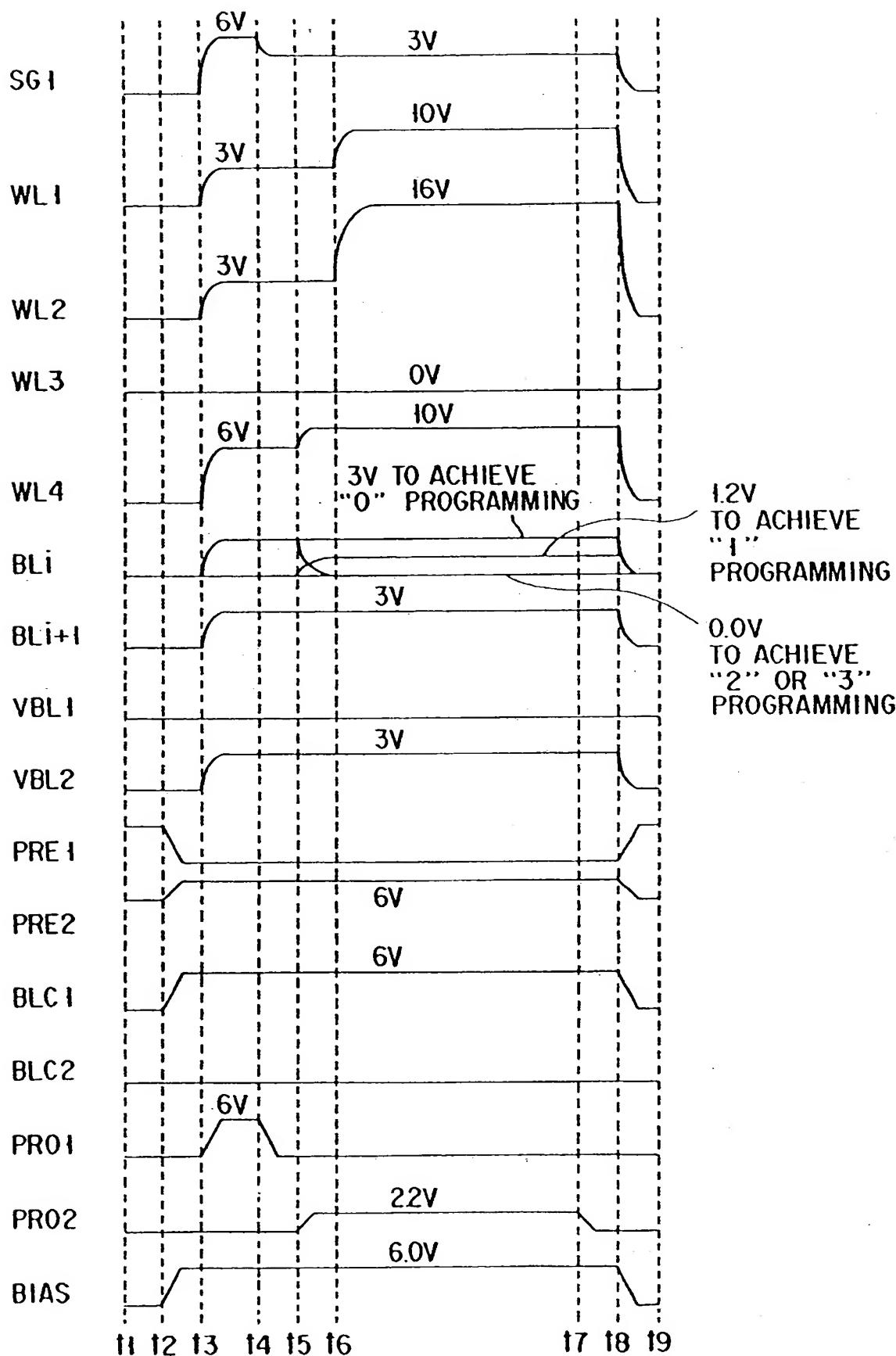


FIG. 12

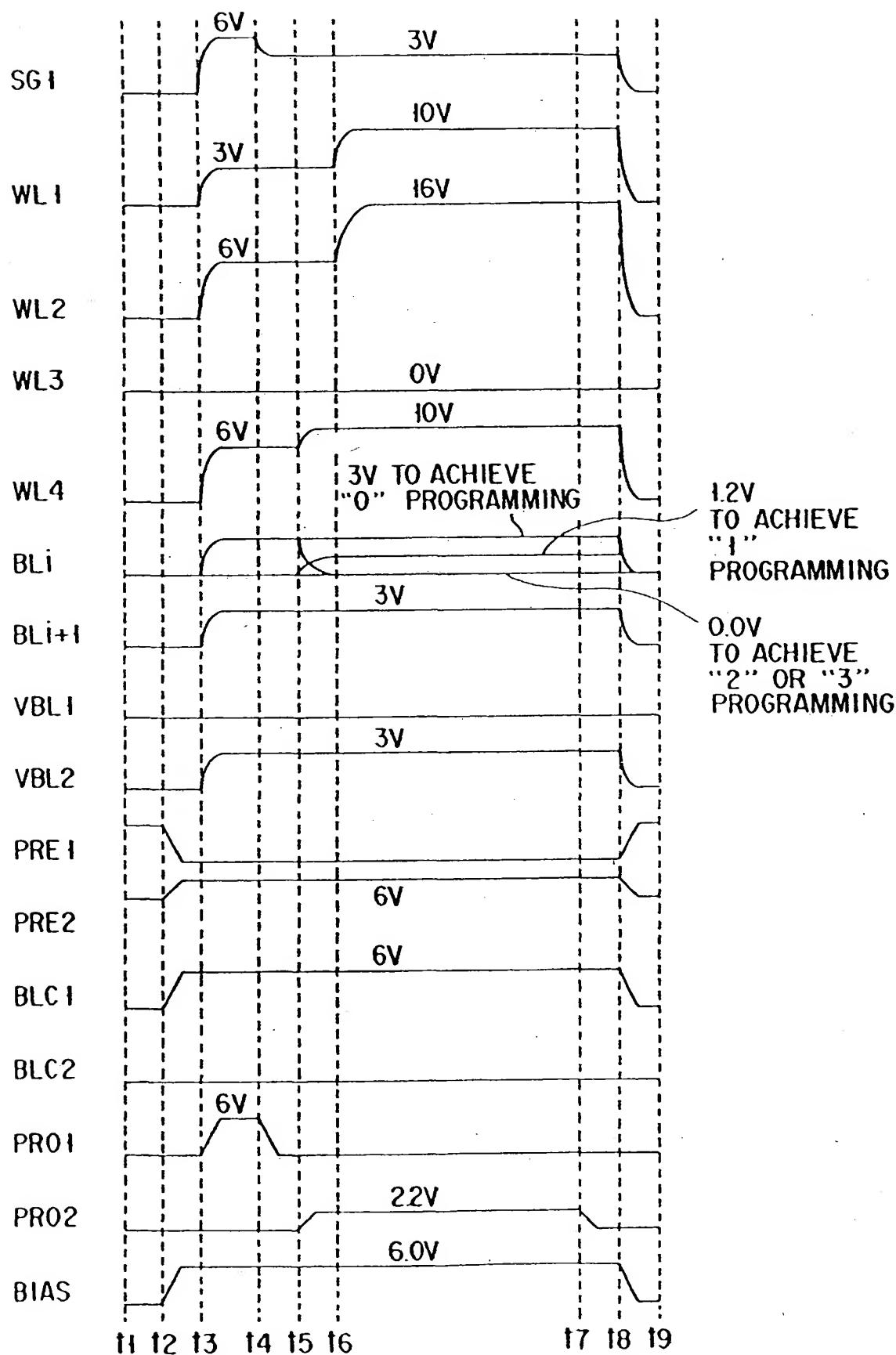


FIG. 13

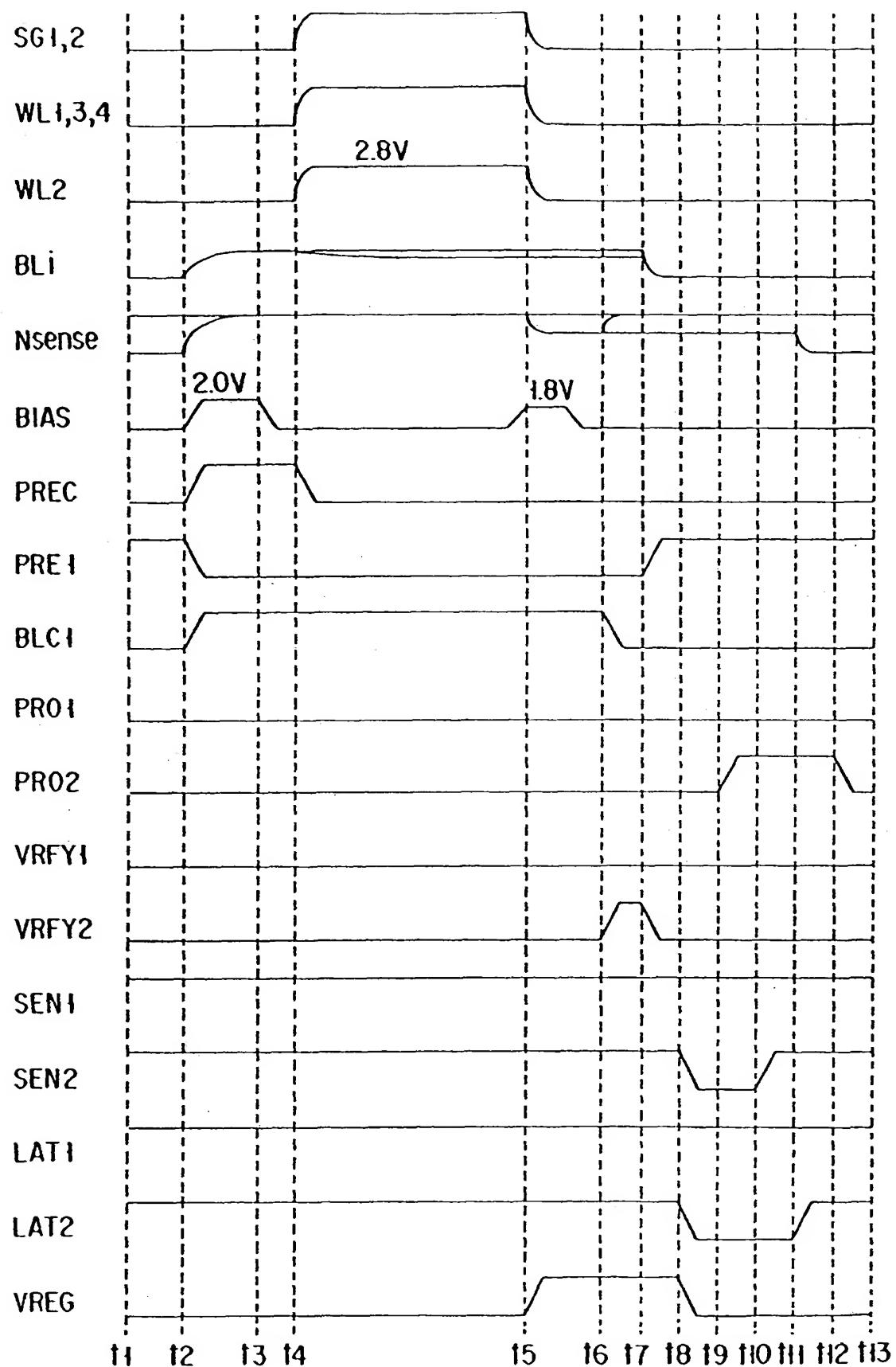


FIG. 14

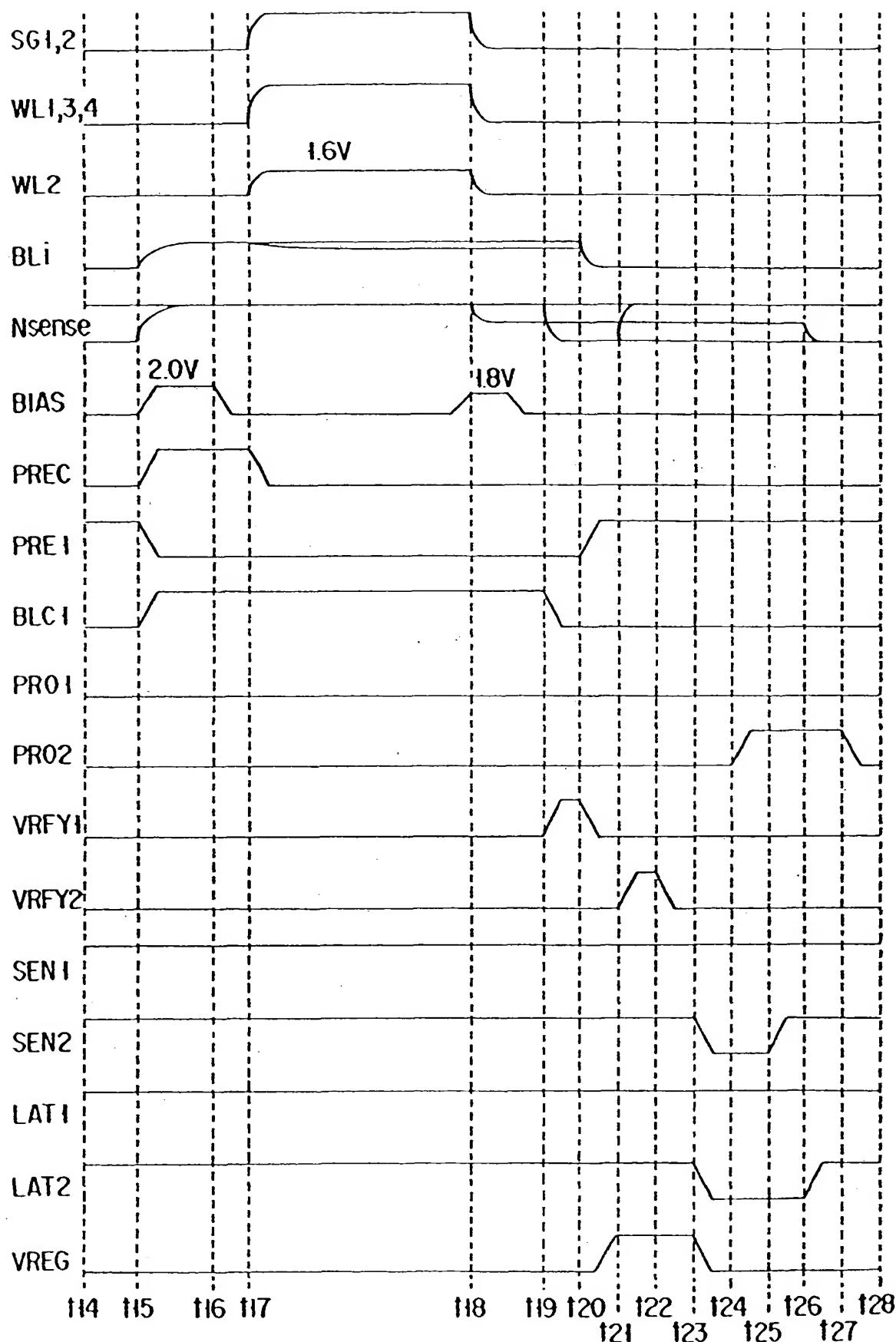


FIG. 15

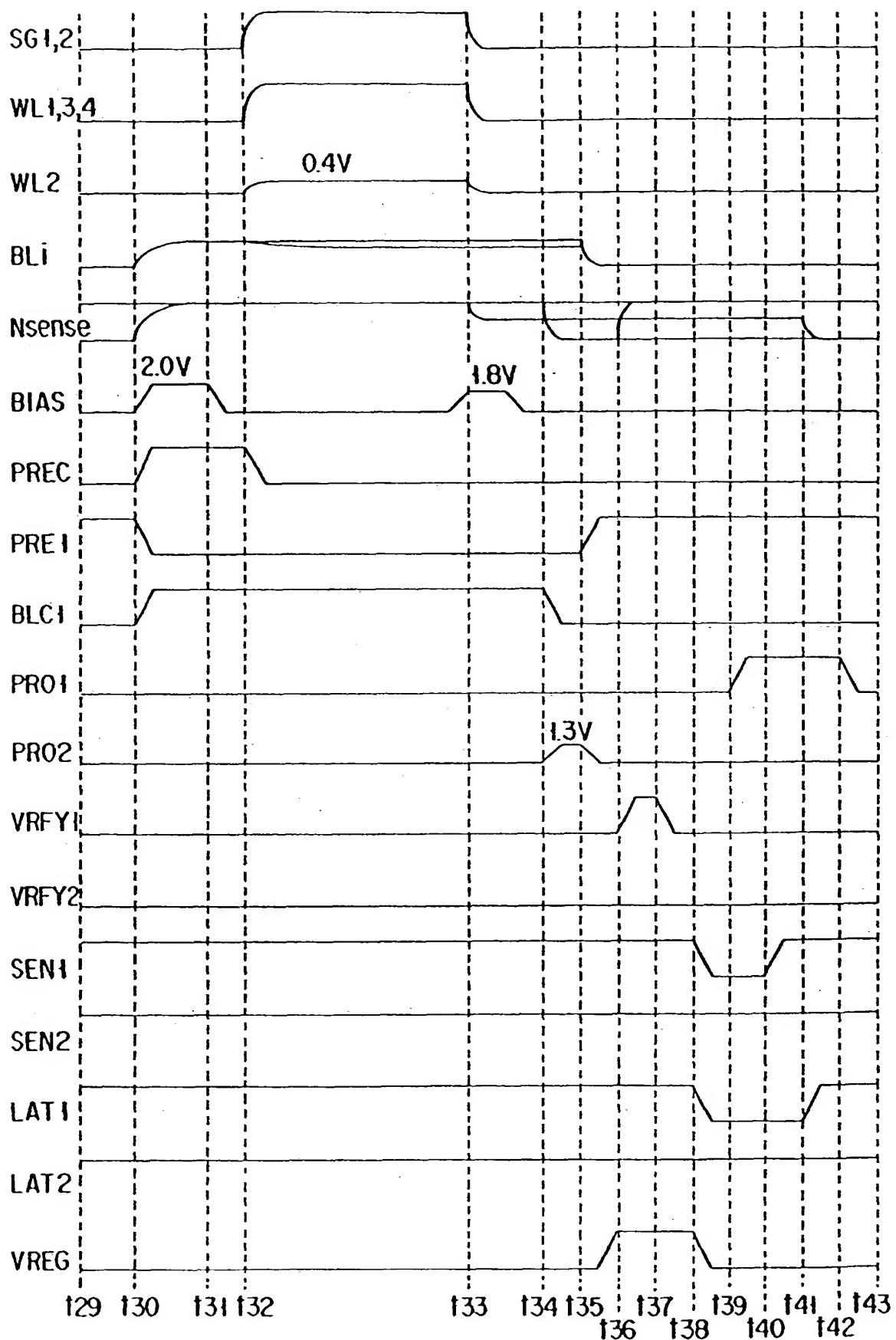


FIG. 16